

Variant 005 (fully populated) shown. For block diagrams of other Variants, refer to the TIDA-01480 Design Guide (TIDUDN1)

Revision History

Rev	ECN #	Approved Date	Approved by	Notes
E1	N/A	2017-11-14	Brian Berner	1st Build
E2	N/A	2017-12-06	Brian Berner	1st Web Release

A

B

C

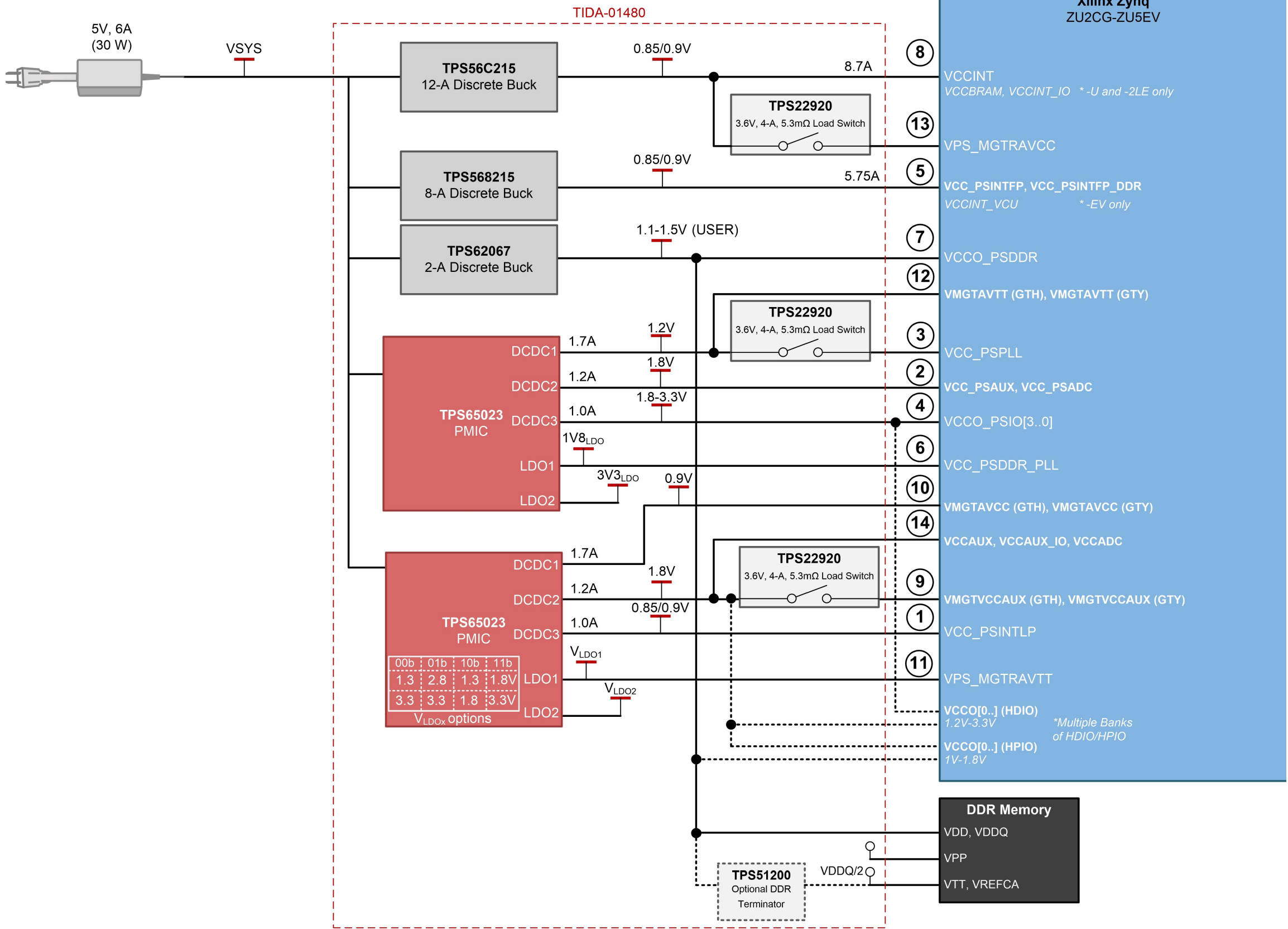
D

A

B

C

D

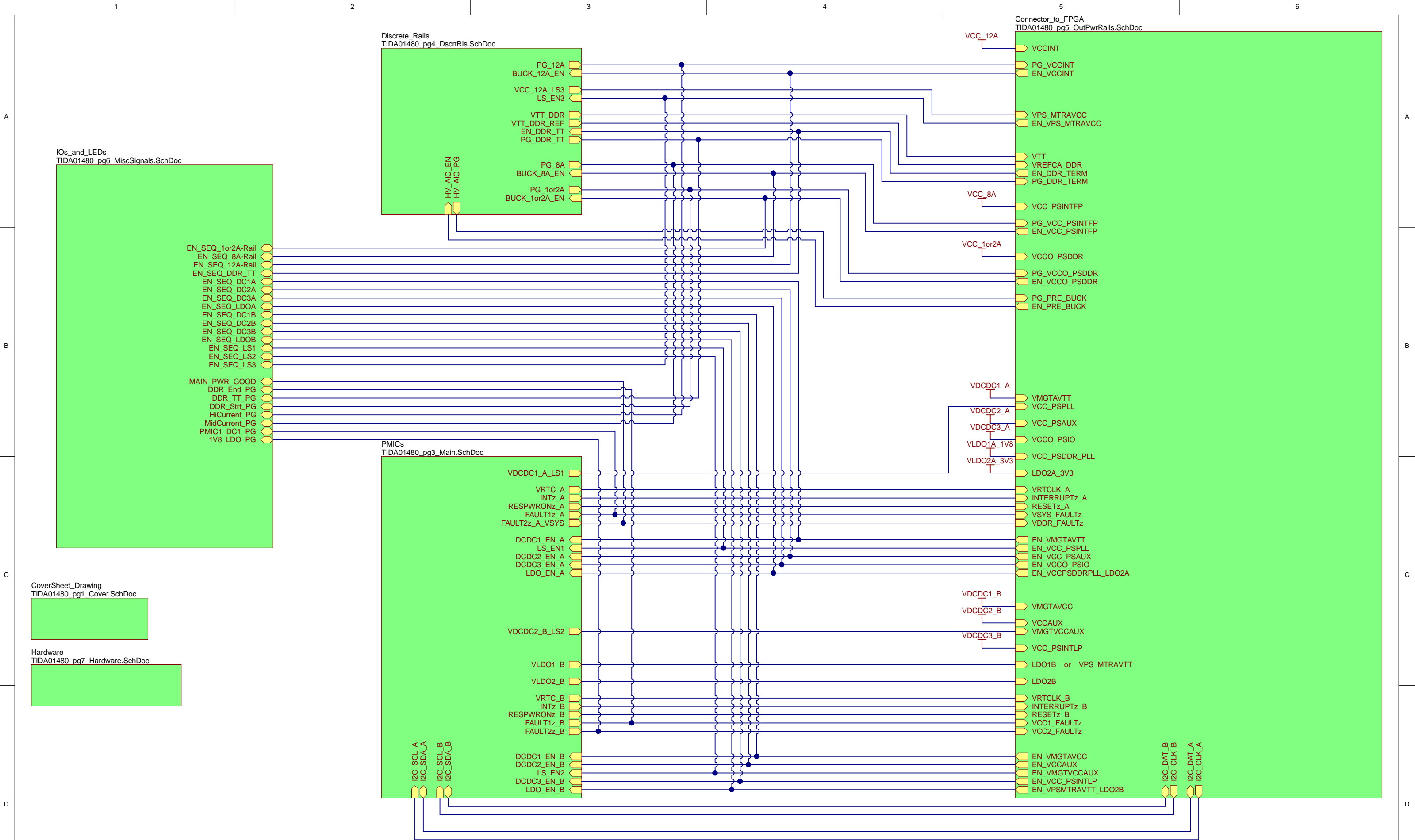


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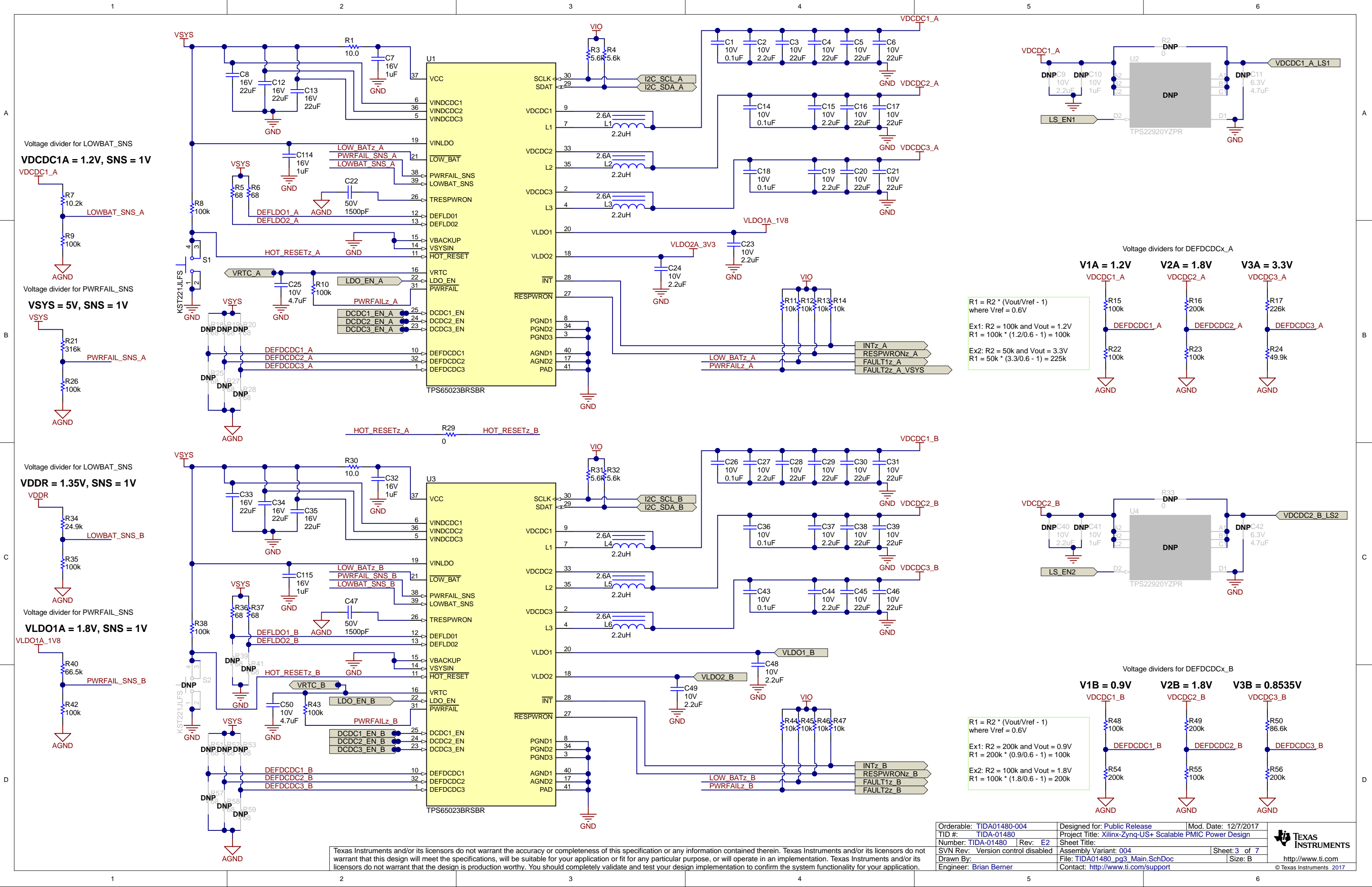
Orderable: TIDA01480-004	Designed for: Public Release	Mod. Date: 12/7/2017
TID #: TIDA-01480	Project Title: Xilinx-Zynq-US+ Scalable PMIC Power Design	
Number: TIDA-01480	Rev: E2	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 004	Sheet: 1 of 7
Drawn By:	File: TIDA01480_pg1_Cover.SchDoc	Size: B
Engineer: Brian Berner	Contact: http://www.ti.com/support	http://www.ti.com
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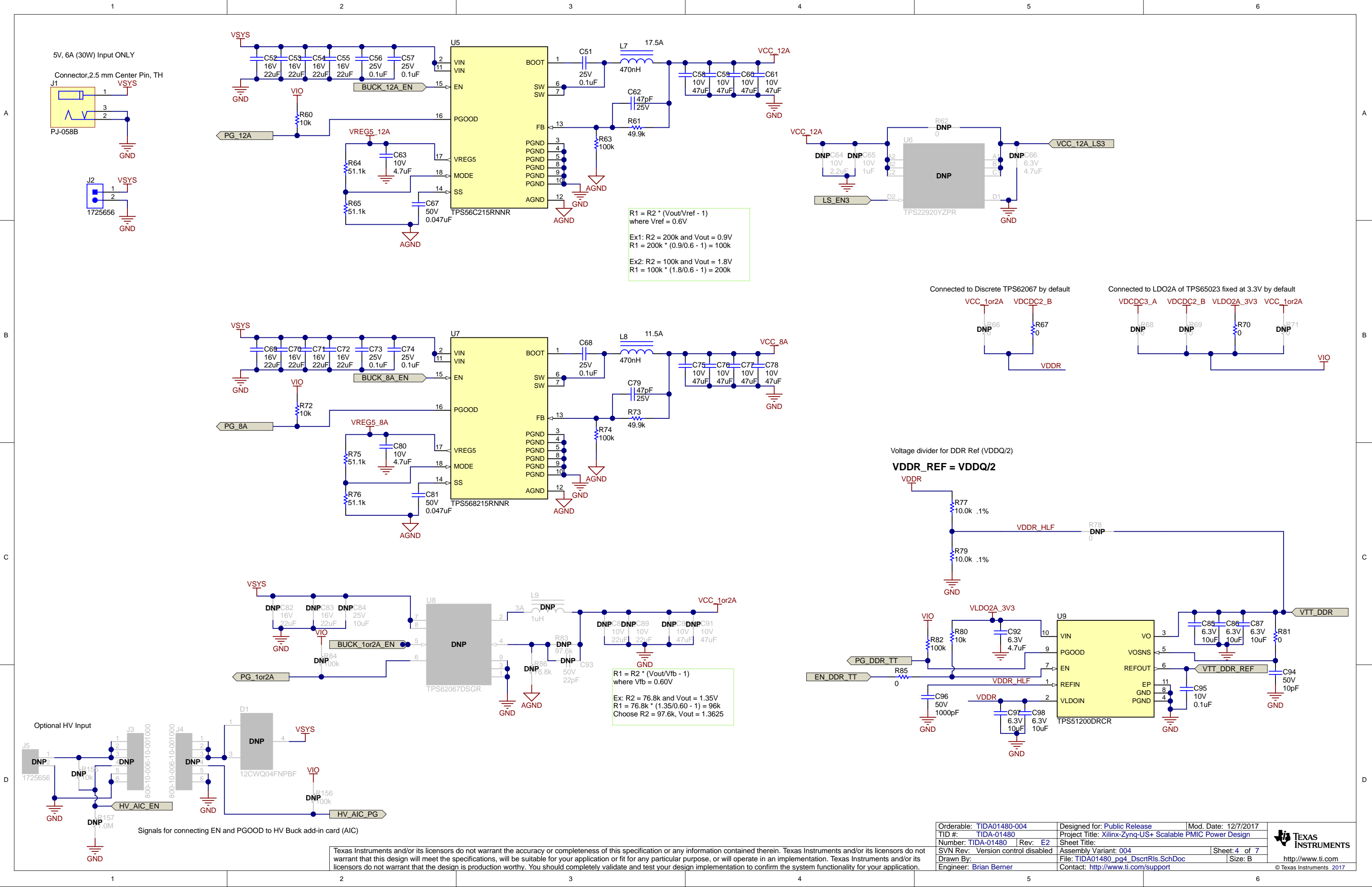


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Number: TIDA-01480	Rev: E2	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 004	Sheet: 2 of 7
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$$R1 = R2 * (V_{out}/V_{ref} - 1)$$

where $V_{ref} = 0.6V$

Ex1: $R2 = 200k$ and $V_{out} = 0.9V$
 $R1 = 200k * (0.9/0.6 - 1) = 100k$

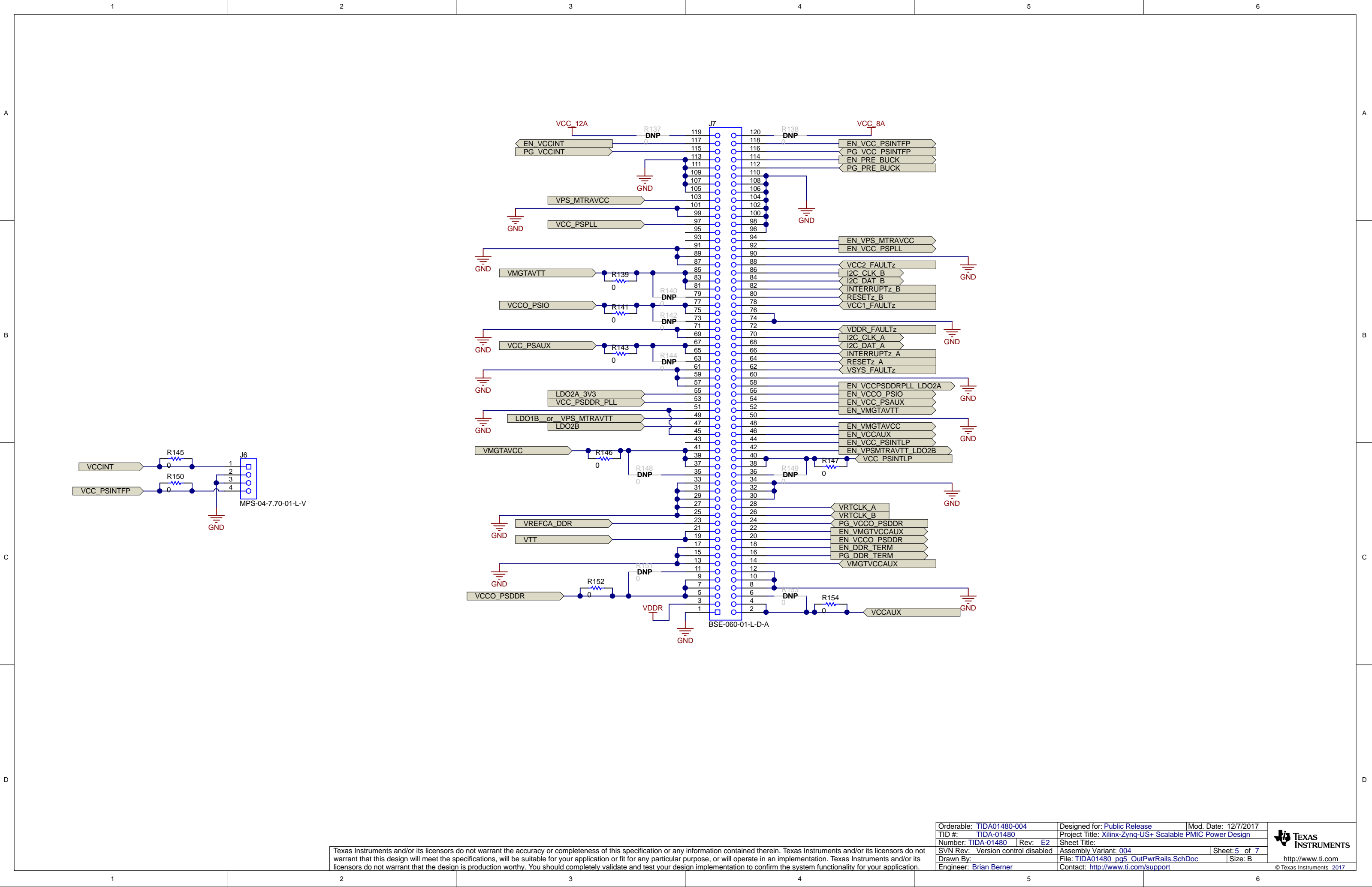
Ex2: $R2 = 100k$ and $V_{out} = 1.8V$
 $R1 = 100k * (1.8/0.6 - 1) = 200k$

$$R1 = R2 * (V_{out}/V_{fb} - 1)$$

where $V_{fb} = 0.60V$

Ex: $R2 = 76.8k$ and $V_{out} = 1.35V$
 $R1 = 76.8k * (1.35/0.60 - 1) = 96k$
Choose $R2 = 97.6k$, $V_{out} = 1.3625$

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SVN Rev: Version control disabled	Assembly Variant: 004	Sheet: 4 of 7
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SVN Rev: Version control disabled	Assembly Variant: 004	Sheet: 5 of 7
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